

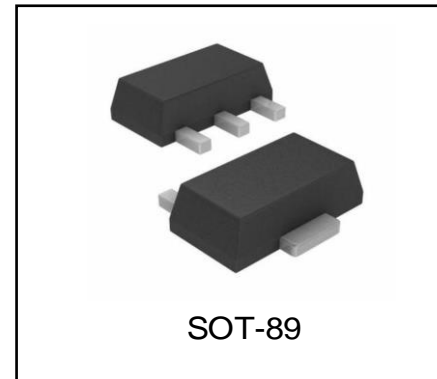
PNP Silicon Transistor

Features

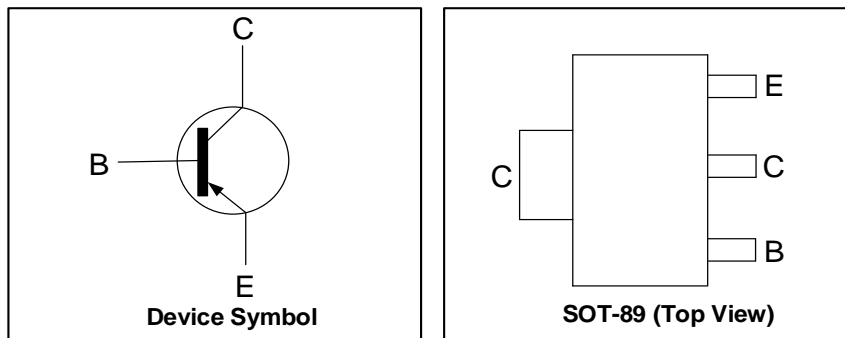
- Complementary to WT5551J
- Switching and Amplification in High Voltage Applications Such as Telephony

Mechanical Characteristics

- SOT-89 Package
- Marking : Making Code
- RoHS Compliant



Schematic & PIN Configuration



Absolute Maximum Rating

Parameter	Symbol	Value	Unit
Collector Base Voltage	V_{CBO}	-160	V
Collector Emitter Voltage	V_{CEO}	-150	V
Emitter Base Voltage	V_{EBO}	-5	V
Collector Current	I_C	-500	mA
Collector Power Dissipation	P_C	500	mW
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55 ~ 150	°C
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	250	°C/W

Electrical Characteristics (T_{amb}=25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C = -100μA, I _E = 0	-160	-	-	V
Collector-emitter breakdown voltage ¹	V _{(BR)CEO}	I _C = -1mA, I _B = 0	-150	-	-	V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E = -10μA, I _C = 0	-5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = -120V, I _E = 0	-	-	-50	nA
Emitter cut-off current	I _{EBO}	V _{EB} = -3V, I _C = 0	-	-	-50	nA
DC current gain ¹	h _{FE(1)}	V _{CE} = -5V, I _C = -1mA	50	-	-	-
	h _{FE(2)}	V _{CE} = -5V, I _C = -10mA	60	-	300	-
	h _{FE(3)}	V _{CE} = -5V, I _C = -50mA	50	-	-	-
Collector-emitter saturation voltage ¹	V _{CES(sat)}	I _C = -10mA, I _B = -1mA	-	-	-0.2	V
		I _C = -50mA, I _B = -5mA	-	-	-0.5	V
Base-emitter saturation voltage ¹	V _{BE(sat)}	I _C = -10mA, I _B = -1mA	-	-	-1	V
		I _C = -50mA, I _B = -5mA	-	-	-1	V
Transition frequency	f _T	V _{CE} = -5V, I _C = -10mA, f = 30MHz	100	-	-	MHz
Output Capacitance	C _{ob}	V _{CB} = -10V, I _E = 0, f = 1MHz	-	-	6	pF
Noise Figure	NF	V _{CE} = -5V, I _C = -200μA, R _s = 1Ω, f = 10Hz to 15.7kHz	-	-	8	dB

Note:

1. Pulse test: pulse width ≤ 300μs, duty cycle ≤ 2.0%

Typical Characteristics

Figure 1. Static Characteristics

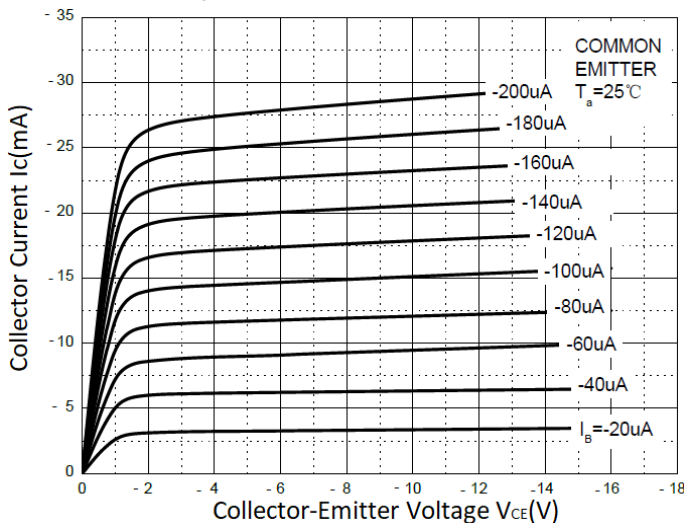


Figure 2. h_{FE} vs. I_C

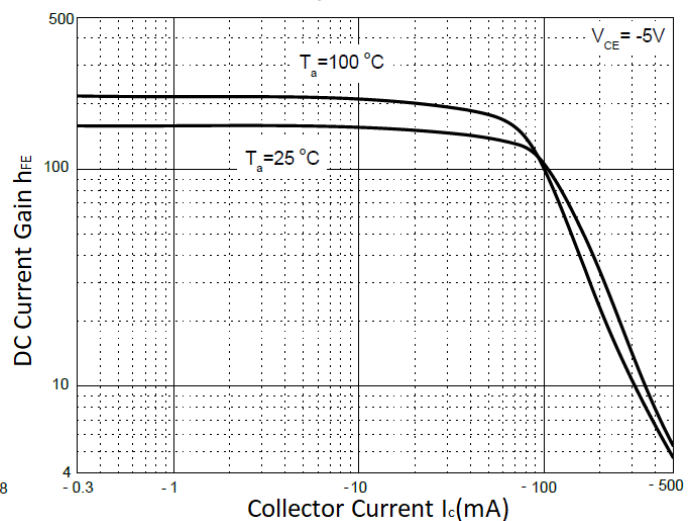


Figure 3. $V_{BE(sat)}$ vs. I_C

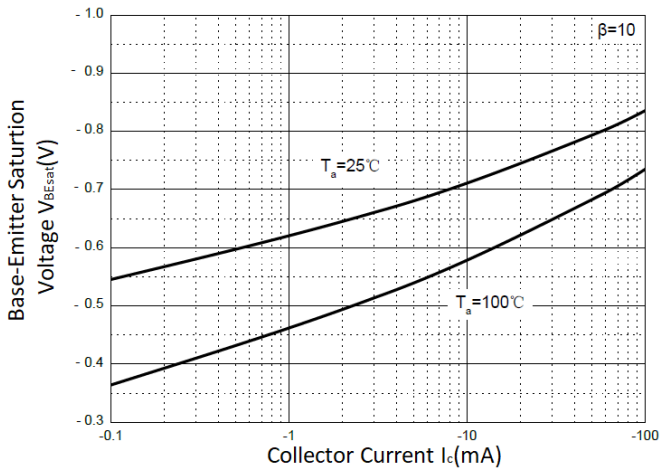


Figure 4. $V_{CE(sat)}$ vs. I_C

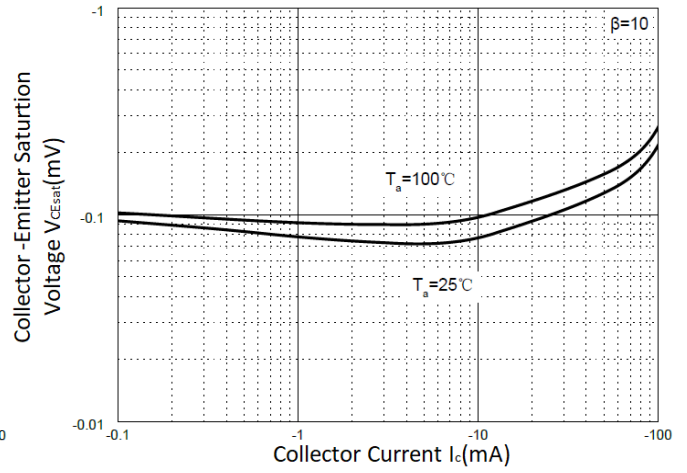


Figure 5. C_{ob}/C_{ib} vs. V_{CB}/V_{EB}

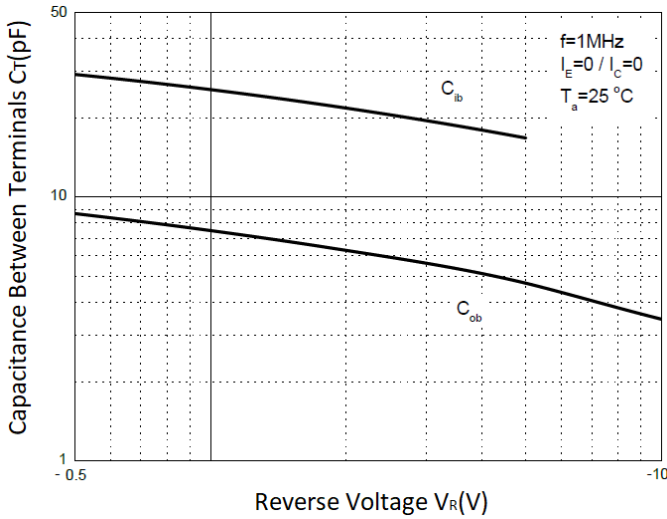


Figure 6. f_T vs. I_C

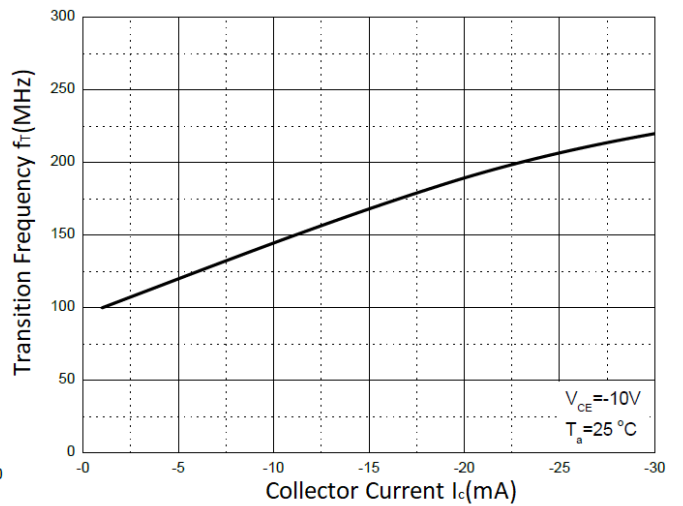
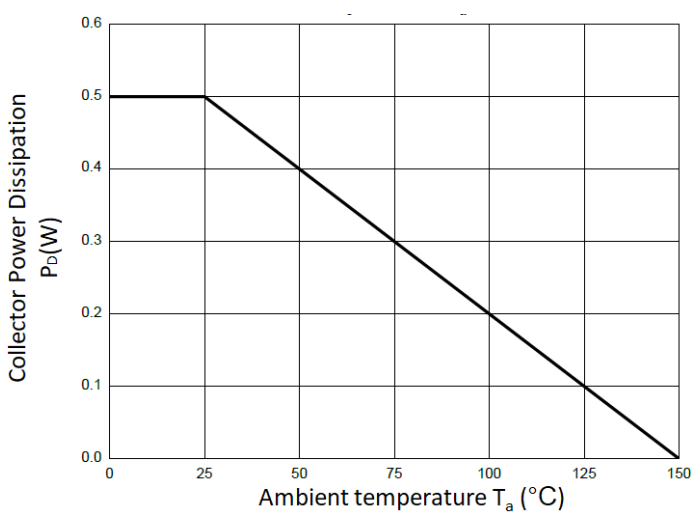
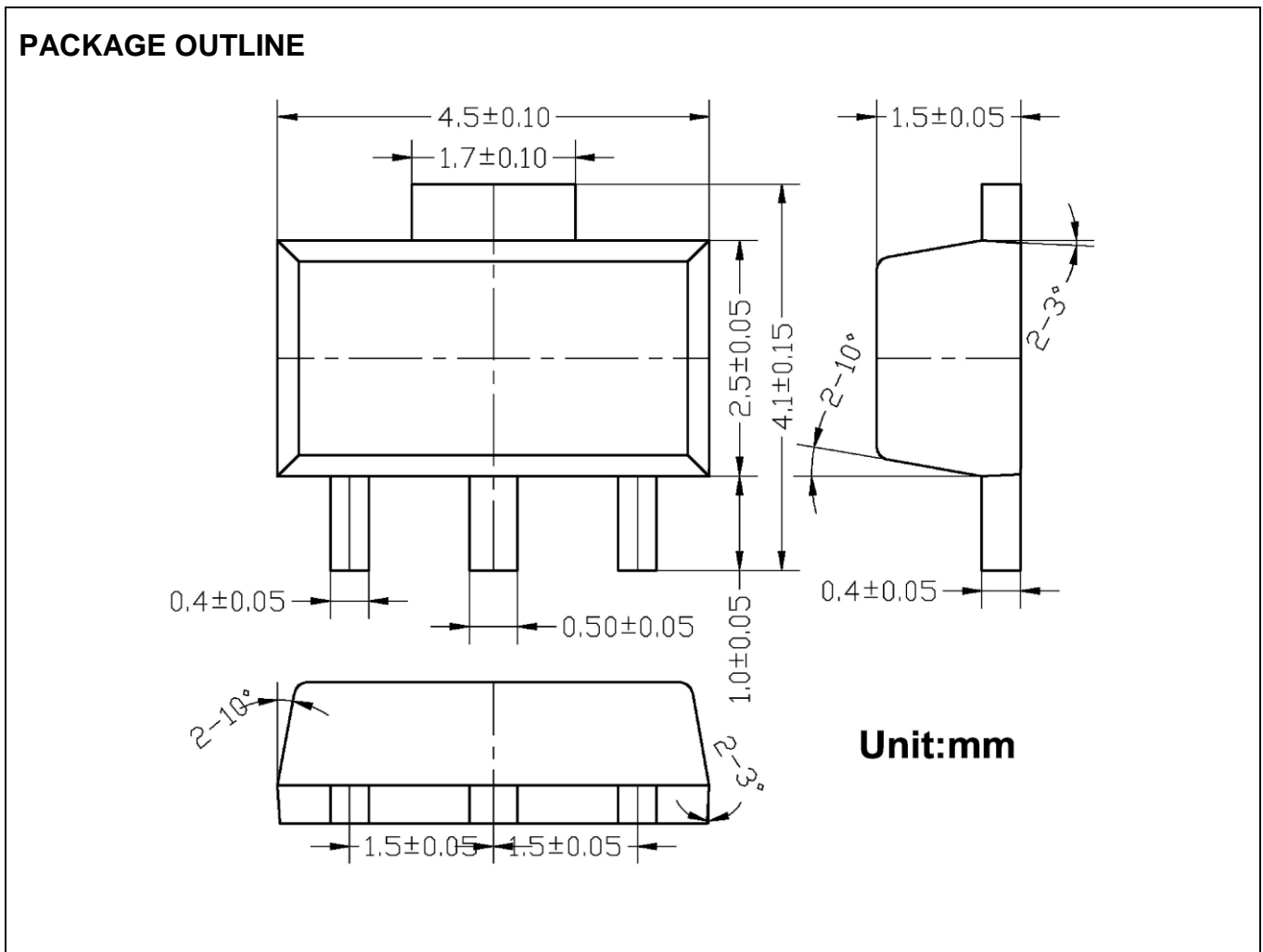


Figure 7. P_D vs. T_a



Outline Drawing – SOT-89



Marking Codes

Part Number	WT5401J
Marking Code	5401

Package Information

Qty: 1k/Reel

CONTACT INFORMATION

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For additional information, please contact your local Sales Representative.

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*Specifications are subject to change without notice.
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
Users should verify actual device performance in their specific applications.*